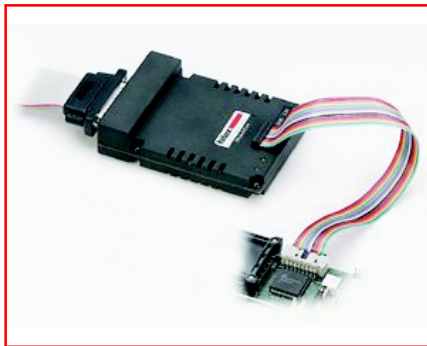


# JProbeXC

The Entry Level Solution for Controllers based on the C166S V2 Core from Infineon

## The OCDS Debug Solution

Derivatives based on the C166S V2 core come complete with an OCDS (On Chip Debug System) debug interface. This interface facilitates debugging based on a JTAG connection to the target system. Hitex's JProbeXC is a universal solution for Infineon's XC166 derivatives that make use of an OCDS. Due to the standardized debug interface, the JProbeXC is already prepared for upcoming devices and it can easily be upgraded by software. The main benefits of this product line are ease-of-use and versatility.



## HiTOP Debugger

Hitex's proprietary user interface HiTOP supports highly sophisticated HLL-debugging while allowing an easy migration between various types of tools and CPU platforms. The HiTOP debugger offers software breakpoints, OCDS hardware breakpoints and it is able to implement fast-breaks for on-the-fly access to any internal register and any

area of memory. HiTOP fully supports on-chip flash memory, which can be directly programmed with user-friendly commands.

## Target Connection

In order to establish a fast link to the target system, the JTAG connector is linked to the host PC's LPT port. Thus a JTAG connector should be available on the target hardware. However, if a JTAG-connector is not available, Hitex offers a direct adaptation to the chip using its patented PressOn technology. This avoids the need for additional space to accommodate a JTAG-connector on the PCB - which could be useful for the debugging of production units.

## User Environments

HiTOP supports all leading compilers and their object formats. Due to HiTOP's wide acceptance as standard emulation control software, many third-party tools have been seamlessly integrated with it. HiTOP also supports all major embedded real-time operating systems, providing a fully transparent overview of an entire software system at any time by displaying information such as task lists, the status of a specific task, message queues, delays and memory objects.

## Also available DProbeXC

Hitex offers also a dedicated DProbeXC with extended trace and break functionality. The DProbeXC is based on the dedicated XC161 emulation device.

# Highlights

- Sophisticated HLL-debugging software:
  - > easy to use
  - > watch/examine for variables
  - > full-blown symbol manager
  - > batch/macro file processing
  - > on-line help function
  - > generator for creating user defined windows
- Fast break for on-the-fly access
- On-chip flash support
- Virtually unlimited software breakpoints
- OCDS hardware breakpoints
- Task-specific debugging
- Adaptation via Hitex PressOn Adapter if no OCDS connector is present on the target
- Supported derivatives: XC161CJ, XC164CS and future XC16x devices



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